

UNIVERSITY OF KERALA

TIME TABLE FOR THE FIRST SEMESTER M.Sc. ELECTRONICS (2014 SCHEME) PRACTICAL EXAMINATION, MAY 2016

(Time 9.30 a.m. onwards)

Day & Date	Subject	Centre	Reg. No.
Wednesday 25.05.2016	PGE 106 Integrated Circuits Lab	CAS, Dhanuvachapuram National College, Manacaud Marthoma College, Ayoor CAS, Adoor CAS, Mavelikkara KVVS, Adoor	All Candidates (including Reappearance candidates)
Thursday 26.05.2016	PGE 107 Communications Engg. Lab	CAS, Dhanuvachapuram National College, Manacaud Marthoma College, Ayoor CAS, Adoor CAS, Mavelikkara KVVS, Adoor	All Candidates (including Reappearance candidates)

CONTROLLER OF EXAMINATIONS

University Buildings,
Thiruvananthapuram,
Dated : 09/05/2016